

DDR Unbuffered DIMM/SO-DIMM/Registered DIMM Validation Compliance Sheet

Must accompany all modules submitted for Web Validation

Please ensure that all cells are completely filled out

Instructions :

1. Enclose a copy of the completed compliance sheet with your DIMM/SO-DIMM/Registered DIMM sample shipment.
2. E-mail a copy of this to the validation lab and Intel.
3. Ship assembled Unbuffered DIMMs(8)/SO-DIMMs(8)/Reg. DIMMs(12) that have been functional tested along with a hardcopy of the submission memo and this compliance sheet to the test lab.

Supplier Info

Module Vendor	PRINCETON TECHNOLOGY
Key Contact Name	SCOTT FAIR
Key Contact Phone	PH: 949-851-7776 CELL: 949-302-1942
Key Contact e-mail	SCOTT@PRINCETONUSA .COM

Unbuffered DIMM/SO-DIMM/Registered DIMM Information

Module Type	<input checked="" type="checkbox"/> Unbuffered <input type="checkbox"/> Registered <input type="checkbox"/> SO-DIMM
Module Part Number	GPM400X64C3/256/H
Module capacity	256MB
SPD revision (Programming should be verified)	1.2
Speed tested (Mhz)	DDR400
Module Configuration (# of devices, 1or2 sided, Banks)	16 DEVICES, 2 SIDED MODULE, 4 INTERNAL BANK - CHIPS
Assembly supplier (if different Module supplier)	PRINCETON TECHNOLOGY
PCB Supplier	BRAIN POWER
Gerber Rev No.	REV 1.1
Gerber Modified from Standard?	YES
Does Module follow Jedec standard (y/n), if so which?	RAW CARD A
ECC (Yes/No)?	NO
PLL & Register Vendor and Part #s (for Reg DIMMs)	N/A
Reason for validation (Initial submission, new process, errata fix, resubmit)	INITIAL SUBMISSION
Functional test result (pass/fail)	PASSED

SDRAM Information

SDRAM Part Number	MT46V32M8TG-5B (validated)
SDRAM Supplier	MICRON
SDRAM Size	256Mb
SDRAM Configuration	32X8
SDRAM Package Type (TSOP2, CSP, uBGA, etc)	TSOP2
SDRAM Mask/Die Revision	G
SDRAM Date Code	0422
SDRAM Speed	DDR 400
SDRAM CAS Latency – tRCD - tRP	3-3-3
Does the device support RAS lockout?	YES
Device supports Concurrent Auto-Precharge?	YES
Device Idd7A spec (ForDDR200)	<input type="checkbox"/> 325ma–225 ma <input type="checkbox"/> 224ma-150ma <input type="checkbox"/> <150ma
Device Idd7A spec (ForDDR266)	<input type="checkbox"/> 425ma–225 ma <input type="checkbox"/> 224ma-150ma <input type="checkbox"/> <150ma
SDRAM Validated?	YES

Please provide accurate web posting information

Supplier	Module Density	SDRAM Configuration	Speed	CL-tRCD-tRD	Module Part Number																					
Eg., XYZ Co.	256MB	256M-32MX8	333	2-2-2	DDR123456-200																					
PRINCETON TECHNOLOGY	256MB	256M-32MX8	400	3-3-3	GPM400X64C3/256/H																					
Comments :																										
For Test Lab Use Only																										
<p>Mail completed forms along with a shipping memo to (Preferably by e-mail) :</p> <table> <tr> <td>Intel</td> <td>&</td> <td>rhonda_duda@validationlabs.com</td> </tr> <tr> <td>c/o Christopher Cox - DIMM Validation Manager</td> <td></td> <td>R. Duda</td> </tr> <tr> <td>1900 Prairie City Rd</td> <td></td> <td>Advanced Validation Labs</td> </tr> <tr> <td>FM5-111</td> <td></td> <td>17800 Newhope Street, Suite B</td> </tr> <tr> <td>Folsom, CA 95630</td> <td></td> <td>Fountain Valley, CA 92708</td> </tr> <tr> <td></td> <td></td> <td>Phone: 714-435-2630</td> </tr> <tr> <td></td> <td></td> <td>Fax: 714-427-3752</td> </tr> </table> <p style="text-align: center;">Mail samples to the lab directly with a paper copy of this memo.</p>						Intel	&	rhonda_duda@validationlabs.com	c/o Christopher Cox - DIMM Validation Manager		R. Duda	1900 Prairie City Rd		Advanced Validation Labs	FM5-111		17800 Newhope Street, Suite B	Folsom, CA 95630		Fountain Valley, CA 92708			Phone: 714-435-2630			Fax: 714-427-3752
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